

Petr Pfeifer (*1975)

- Ing: 2001, Measurement and Instrumentation from Czech Technical University in Prague (ČVUT).
- Employed in STMicroelectronics and Tyco International in management and senior R&D positions.
- PhD studies from 1.10.2010, in TU of Liberec, Technical Cybernetics.
- Final State Exam (RIGO) passed on 7.2.2012
- Long-term abroad stay in Cottbus in 2012
- Short-term stay in IMEC Leuven in 2013 and other cooperating departments (TTU, BTU, UPS...)

- Doctoral thesis: RELIABILITY ASSESSMENT AND ADVANCED MEASUREMENTS IN MODERN NANOSCALE FPGAS

- His interest and professional work covers areas from design and manufacturing of digital VLSI ASIC, advanced systems using field programmable gate arrays and complex programmable logic devices, automotive and safety systems, signal processing on multicore platforms and reliability aspects of dependable systems using modern submicrometer technologies.

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